

EsimFPD

(Schematic Simulation Tool Kit)

EsimFPD includes four parts:

- EsimFPD SE :Schematic Editor, this tool supports the design of hierarchical circuit. It is more convenient to create panel circuit, control circuit and drive circuit, as shown in Fig1.

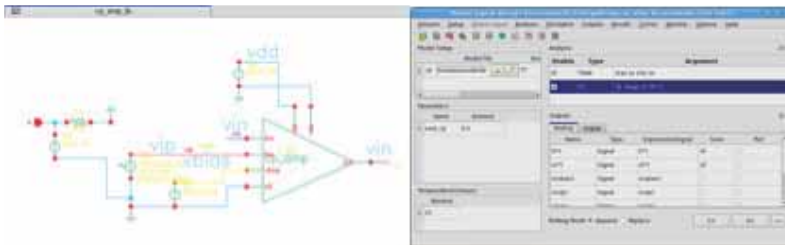


Fig1- Flow of Schematic Simulation

- EsimFPD ALPS: State-of-the-art simulation technology with unprecedented simulation speed. Support tens of millions transistors with true spice accuracy. Seamlessly integration to EsimFPD SE. Easily of use, plug & play. Support power analysis. ALPS also provide CASCADE mode which supports full panel simulation with spice level accuracy.

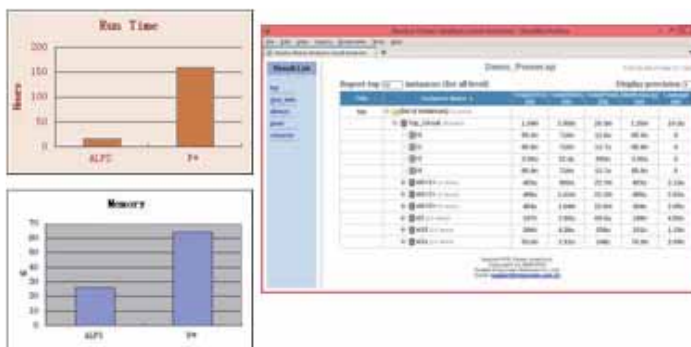


Fig2-Full Panel Simulation And Power Analysis

Advantages:

- Support loop circuit to create hierarchical circuit more convenient
- Easy to generate symbol
- High-Performance SPICE Simulator, compatible with a-Si/LTPS/IGZO/OLED model
- Supply cascade description to realize full panel simulation quickly
- Support multi-core parallel simulation to realize power analysis quickly
- Support display massive simulation results
- Support to extract a-Si/LTPS/IGZO/OLED HMI model
- Support model analysis and quality assurance

- EsimFPD iWave: The waveform viewer is an analog and mixed-signal SpiceExplorer and WaveView analyzer, providing customers with the simulation results display, measurement, calculation, edit and save operations. Moreover, multi-panel waveform display, waveform stack, waveform conversions (A-D, D-A) and wave comparing are also supported, as shown in Fig3.

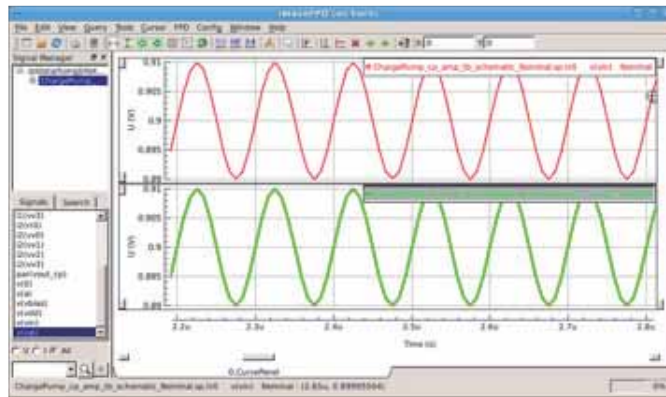


Fig3-Wave Viewer iWaveFPD

- EsimFPD Model: The model extractor, it supports to extract model card of HMI and RPI. It can help users to realize model analysis and quality assurance, as shown in Fig4.

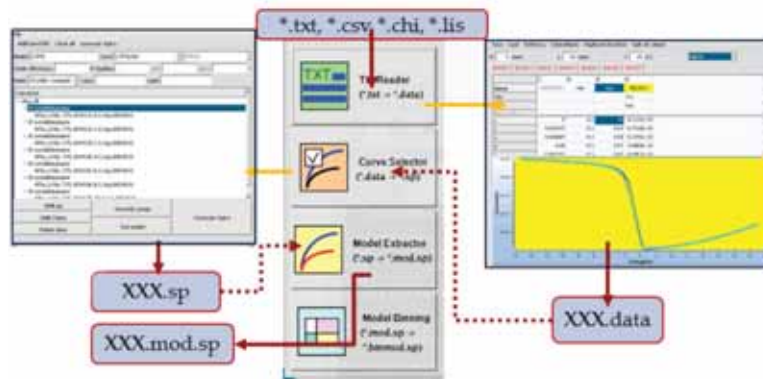


Fig4-The flow of Model Extractor